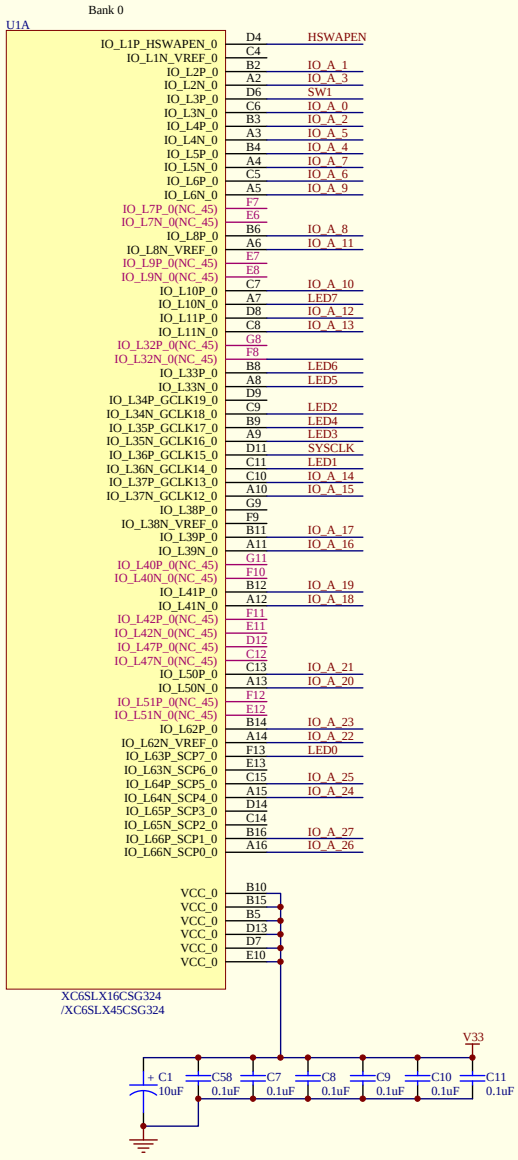
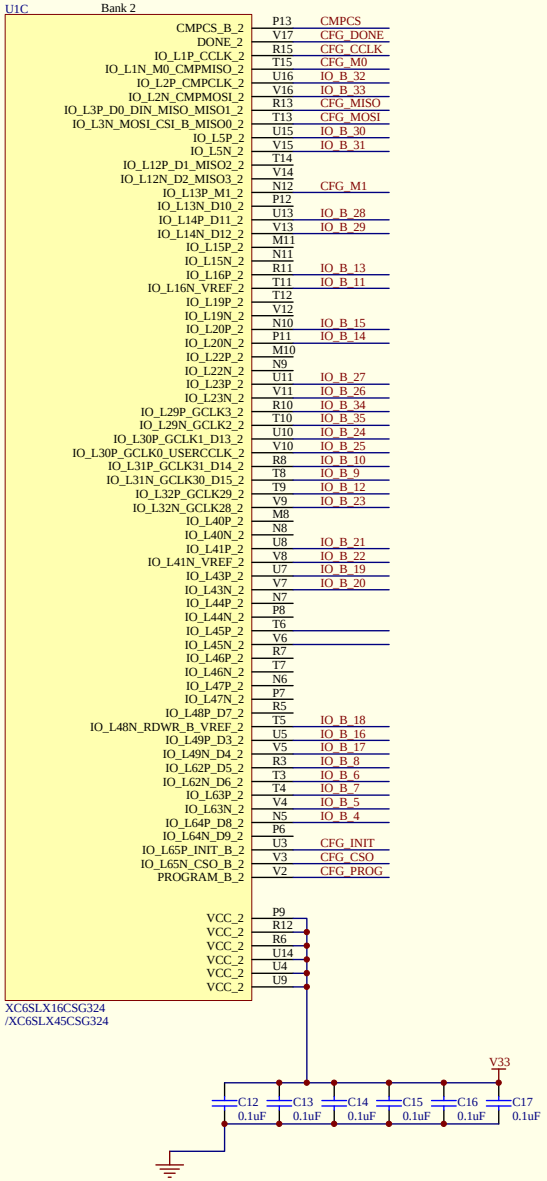
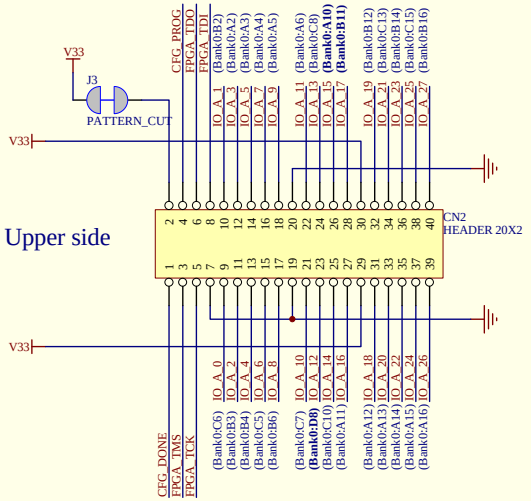
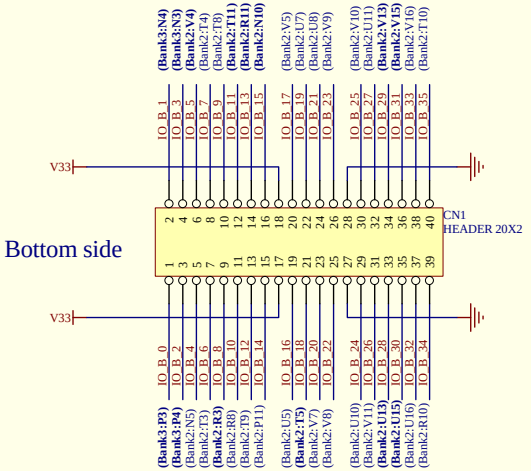



If short J3, 3.3V power is supplied from CN2_2 pin.

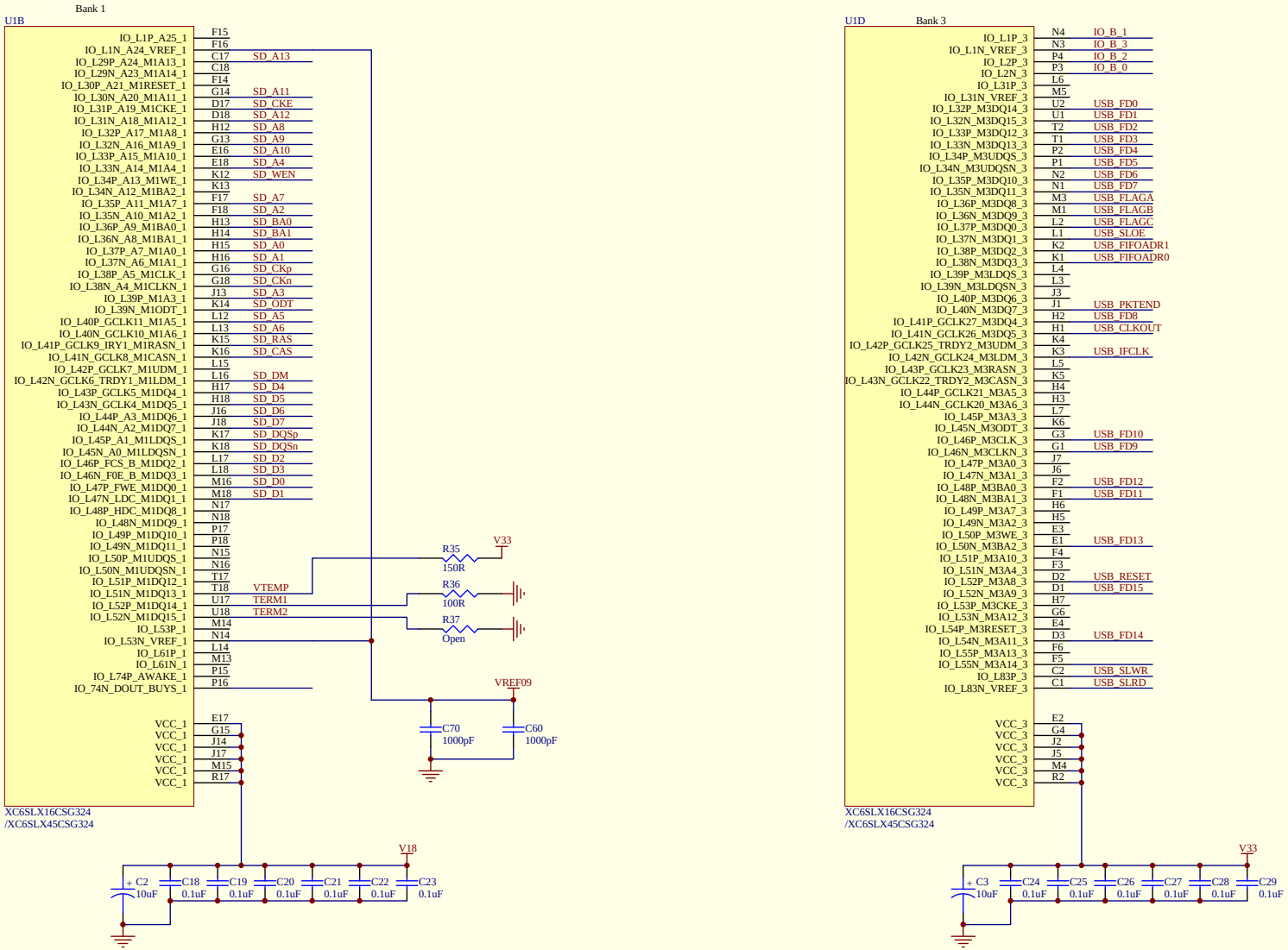



TOKUDEN Spartan-6 Evaluation board

(C)Copyright 2009-2010 TokushuDenshiKairo Inc.

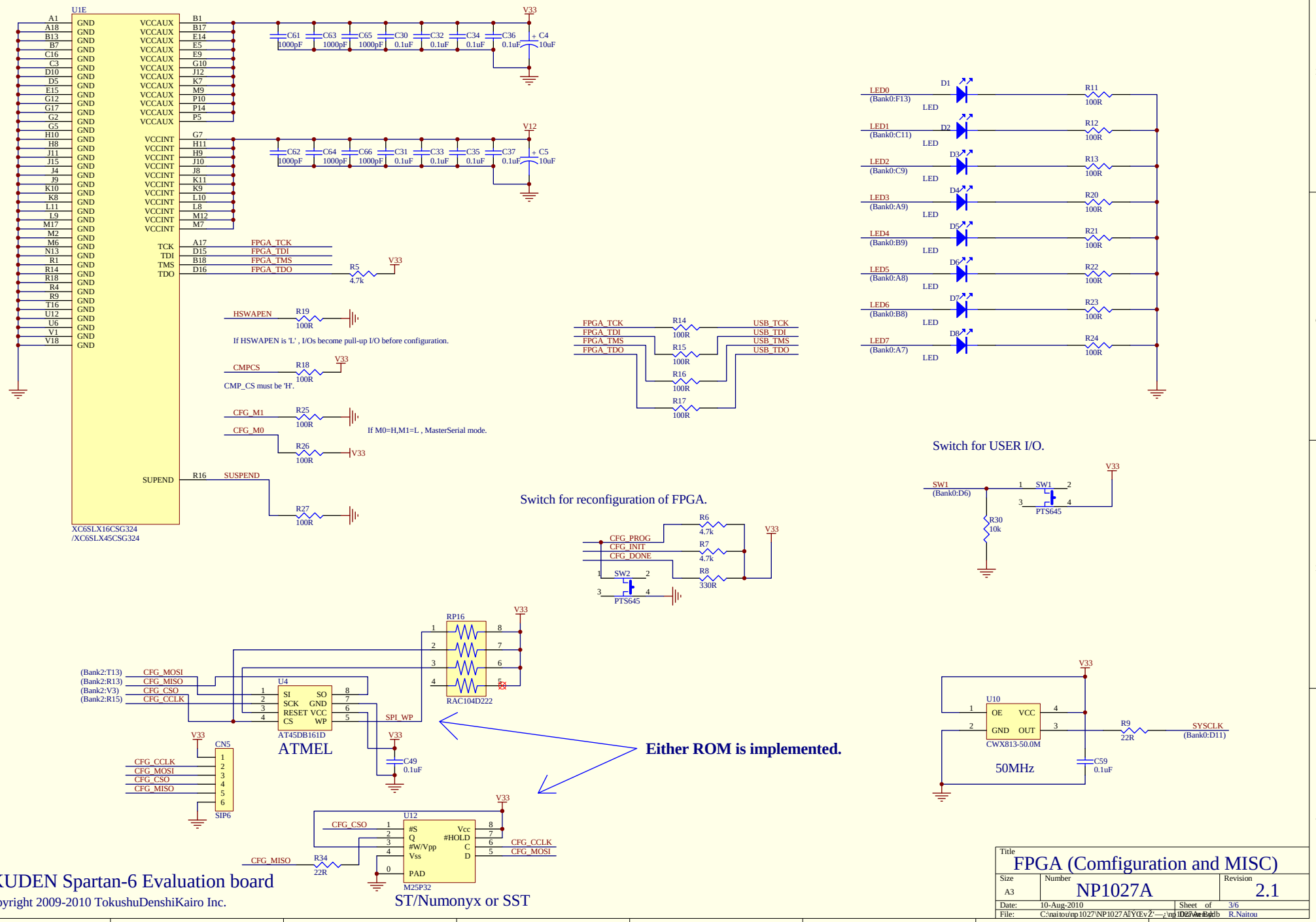
| | | |
|---|---|---|
| Title | FPGA (GPIO pin headder) | |
| Size: A3 | Number: NP1027A | Revision: 2.1 |
| Date: 10-Aug-2010 | Sheet of 1/6 | |
| File: C:\naitou\np1027\NP1027A.SchDoc | Drawn By: R.Naitou | |




TOKUDEN Spartan-6 Evaluation board

(C)Copyright 2009-2010 TokushuDenshiKairo Inc.

| | | |
|---|---|---|
| Title | FPGA (USB and DDR2 SDRAM) | |
| Size: A3 | Number: NP1027A | Revision: 2.1 |
| Date: 10-Aug-2010 | Sheet of 2/6 | |
| File: C:\naitou\np1027\NP1027A[ÝÇÉvÍ[—\np1027a.sch | | R.Naitou |




TOKUDEN Spartan-6 Evaluation board

(C)Copyright 2009-2010 TokushuDenshiKairo Inc.

| | | |
|---|---|---|
| Title | FPGA (Comfiguration and MISC) | |
| Size: A3 | Number: NP1027A | Revision: 2.1 |
| Date: 10-Aug-2010 | Sheet of 3/6 | |
| File: C:\naitou\np1027\NP1027A[YCEv2'—¿\np1027ver2.0.db | | R.Naitou |